Simulator Requirement Specification

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| --- | --- |
| Department Name | Computer Science and Engineering Lab |
| Class | S.Y.BTech |
| Semester | 1 |
| Subject Name | Data Structures Lab |
| Experiment No. | 1 |
| Experiment Name | Pointers |

Version History

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| --- | --- | --- | --- | --- |
| Sr. No. | Version Number | Created By | Approved By | Date |
| 1 | v1.0 | Jay Deelip Kamble | Prof. Shivani Kale Ma’am | 04/11/2020 |
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Detailed Requirement Specification

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| **Req. ID** | **Visual Entity Required** | **Requirement Description** | **Comments** |
| REQ1 | Heading | To display the name of simulator | - |
| REQ2 | Text area | To display the code | - |
| REQ3 | Back arrow button | To go back to a specific line of code | - |
| REQ4 | Forward arrow button | To go to a specific line of code | - |
| REQ5 | Output area | To display output of the code | - |
| REQ6 | Summary area (diagrams) | To show the changes that take place in the memory | - |
| REQ7 | Explaination area | To display a brief explaination about a particular line of code | - |
| REQ8 | Highlighter | To highlight a particular line of code | - |